

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of		)	
	Nobuhiro Nunoya et al.	)	
Serial No.:	10/527,355	)	Art Unit
		)	2828
Filed:	March 7, 2005	)	
		)	
Confirmation No.:	4938	)	
		)	
For:	OPTICAL SEMICONDUCTOR DEVICE AND OPTICAL SEMICONDUCTOR INTEGRATED CIRCUIT	)	
		)	
Examiner:	Patrick Stafford	)	

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

Applicant respectfully disagrees in part with the Examiner's Statement of Reasons for Allowance as set forth in the communication mailed on March 10, 2010.

The Applicant concurs with the Examiner's conclusion that the prior art does not suggest or render obvious the claimed invention. However, Applicant respectfully submits that the claimed invention as set forth in each of the allowed independent and dependent claims must be read as a whole, and not as a single feature or subcombination of features which represent less than the entirety of the claimed invention as a whole. While a particular feature or subcombination of features referred to by the Examiner in the Statement of Reasons for Allowance may represent a basis for distinguishing the claimed invention over the prior art, Applicant submits that this may not necessarily be the *sole* ground for distinguishing the claimed invention over the prior art of record. Accordingly, the Examiner's statement should, in Applicant's view, not be read as constituting or meaning that the invention can or should be reduced to a single "feature" of the invention or to a subcombination of features that is less than the entire invention claimed as a whole, nor that the single feature referenced by the Examiner or subcombination of features referenced by the Examiner in the Statement of Reasons for Allowance is the only or sole grounds for distinguishing the invention over the prior art of record.

Dated this 28th day of April 2010.

Respectfully submitted,

/Scott A. Woodbury/ Reg. #55743  
SCOTT A. WOODBURY

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SAW:cad